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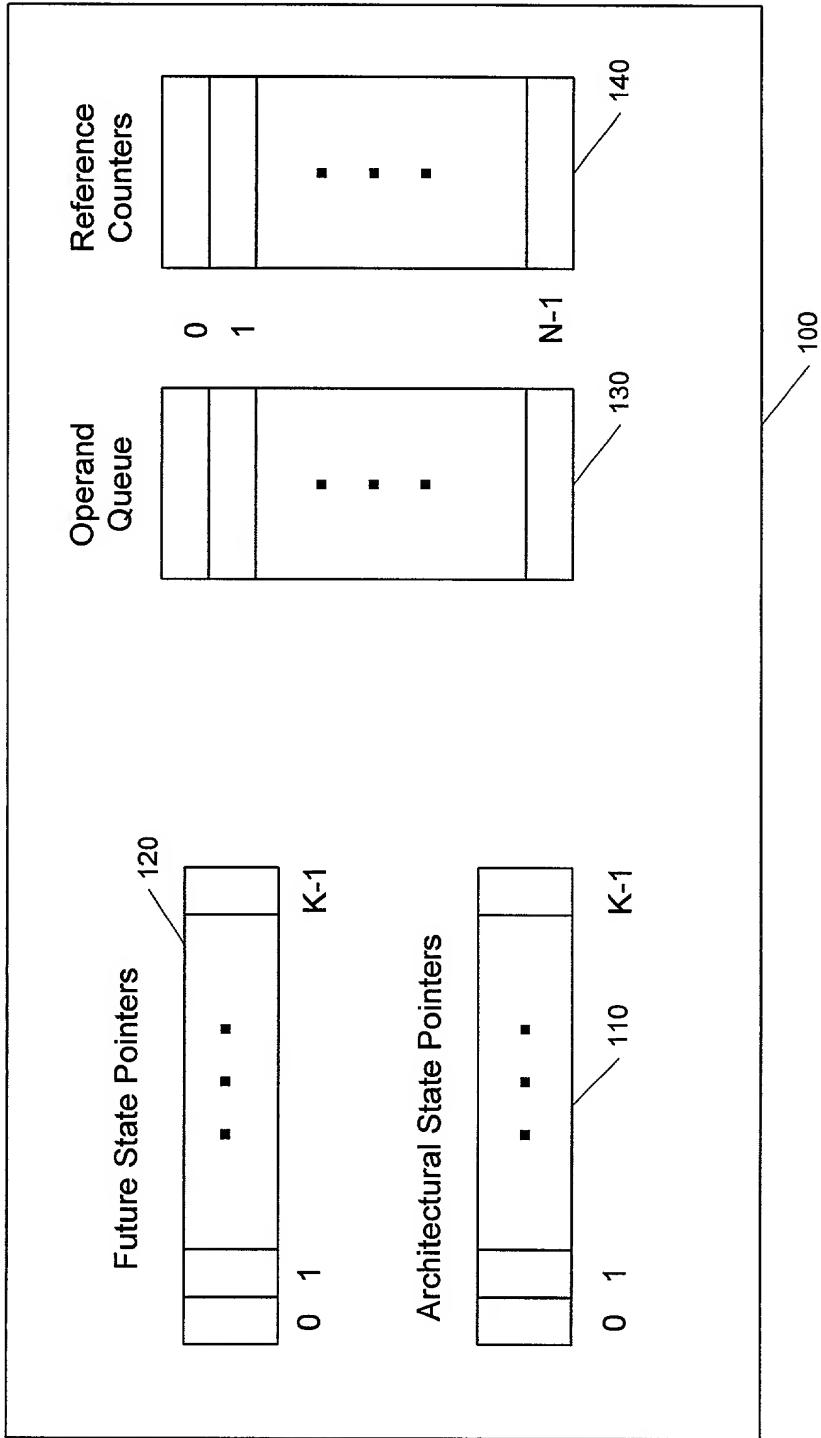


Figure 1

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Future State  
Pointers

		0
		■
		■

Architectural State  
Pointers

0	1		
K-1	K-2		
		0	0

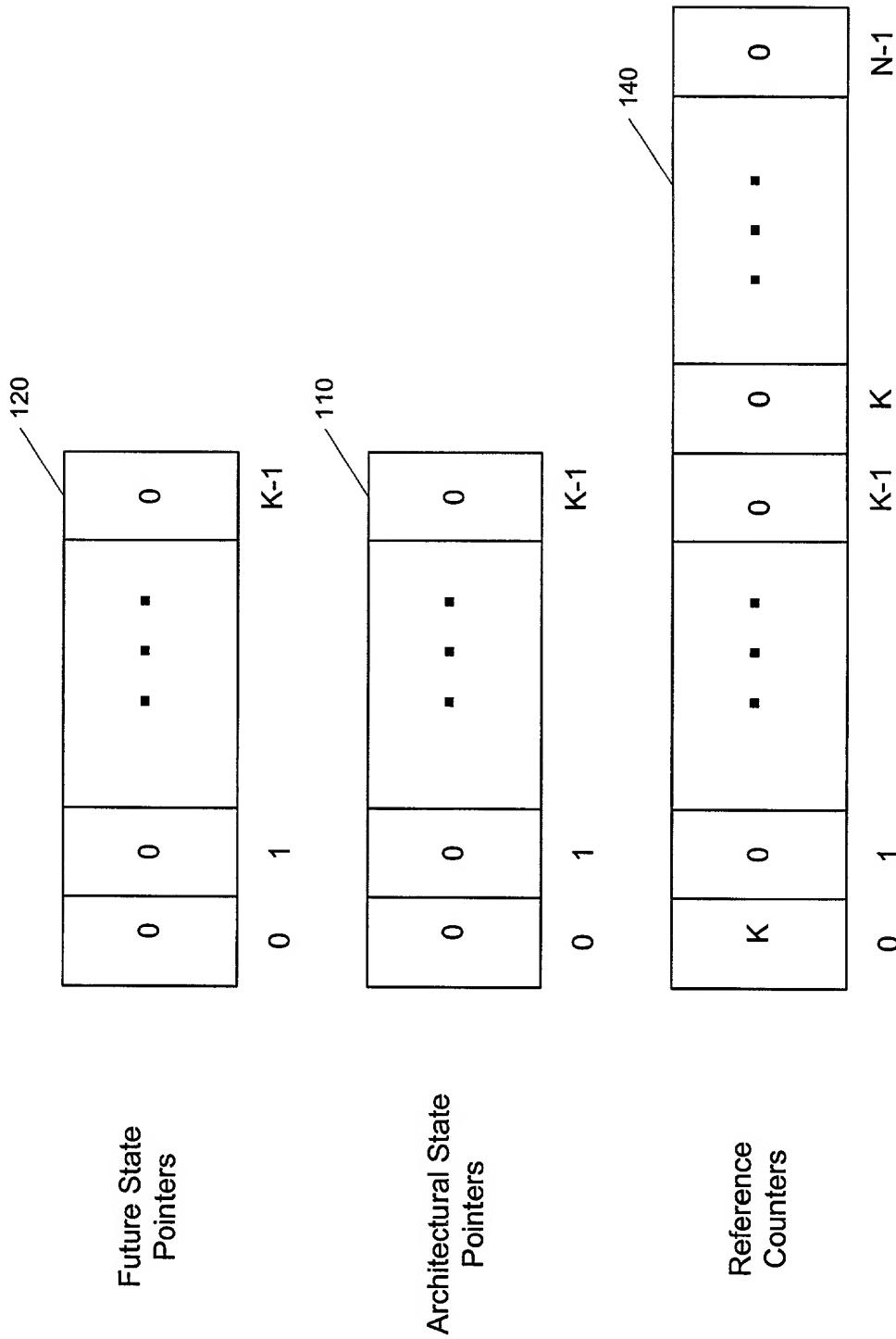
Reference  
Counters

$K-2$	$K-1$	$K$	
1	1	0	0
	1	1	1
		1	0
			0
			0
			0
			0

**Figure 2**

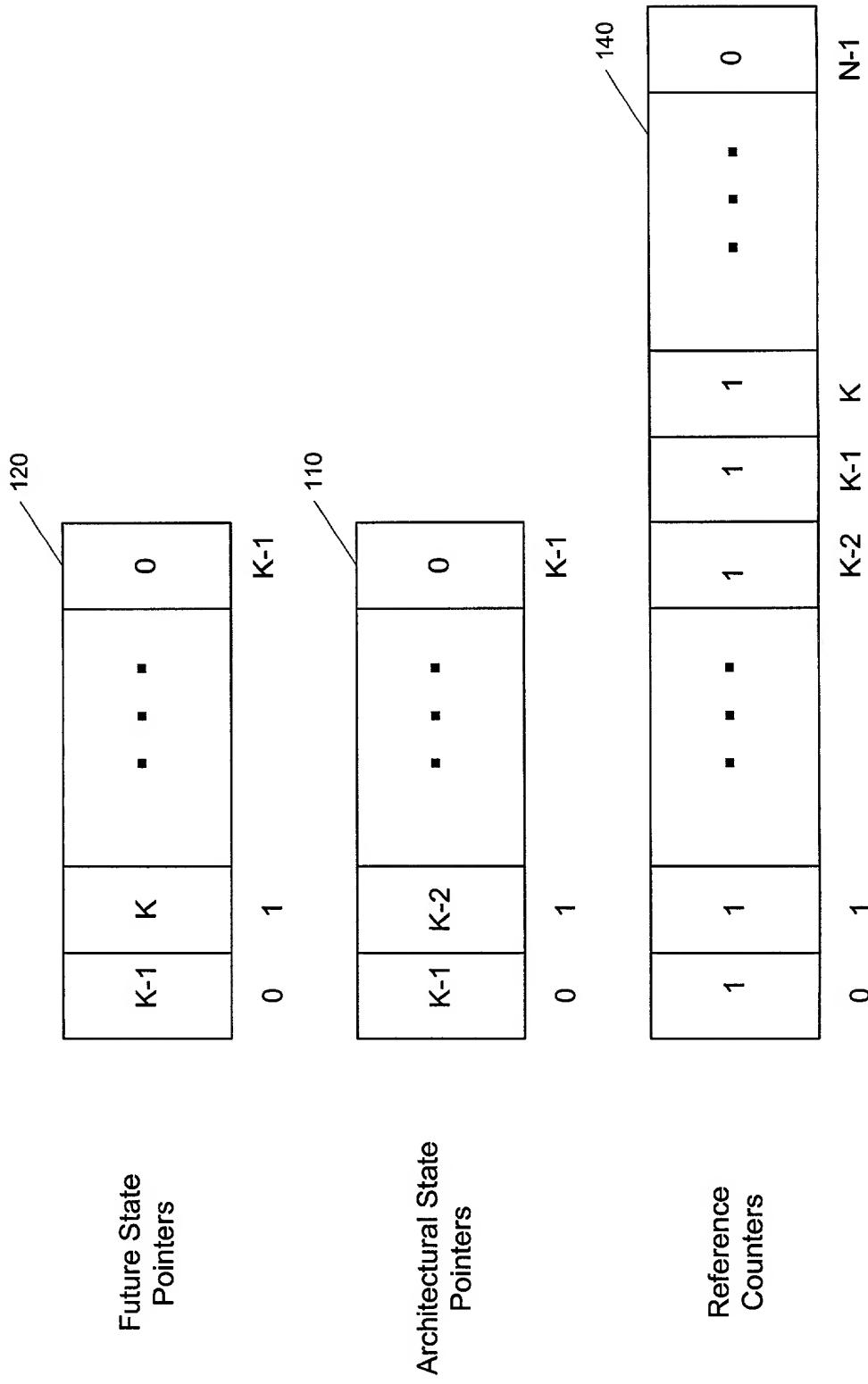
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Figure 3 shows three memory structures: Future State Pointers, Architectural State Pointers, and Reference Counters. Each structure is represented by a grid of boxes. The columns are labeled 0, 1, ..., K-1, K. The rows are labeled 0, 1, ..., N-1.

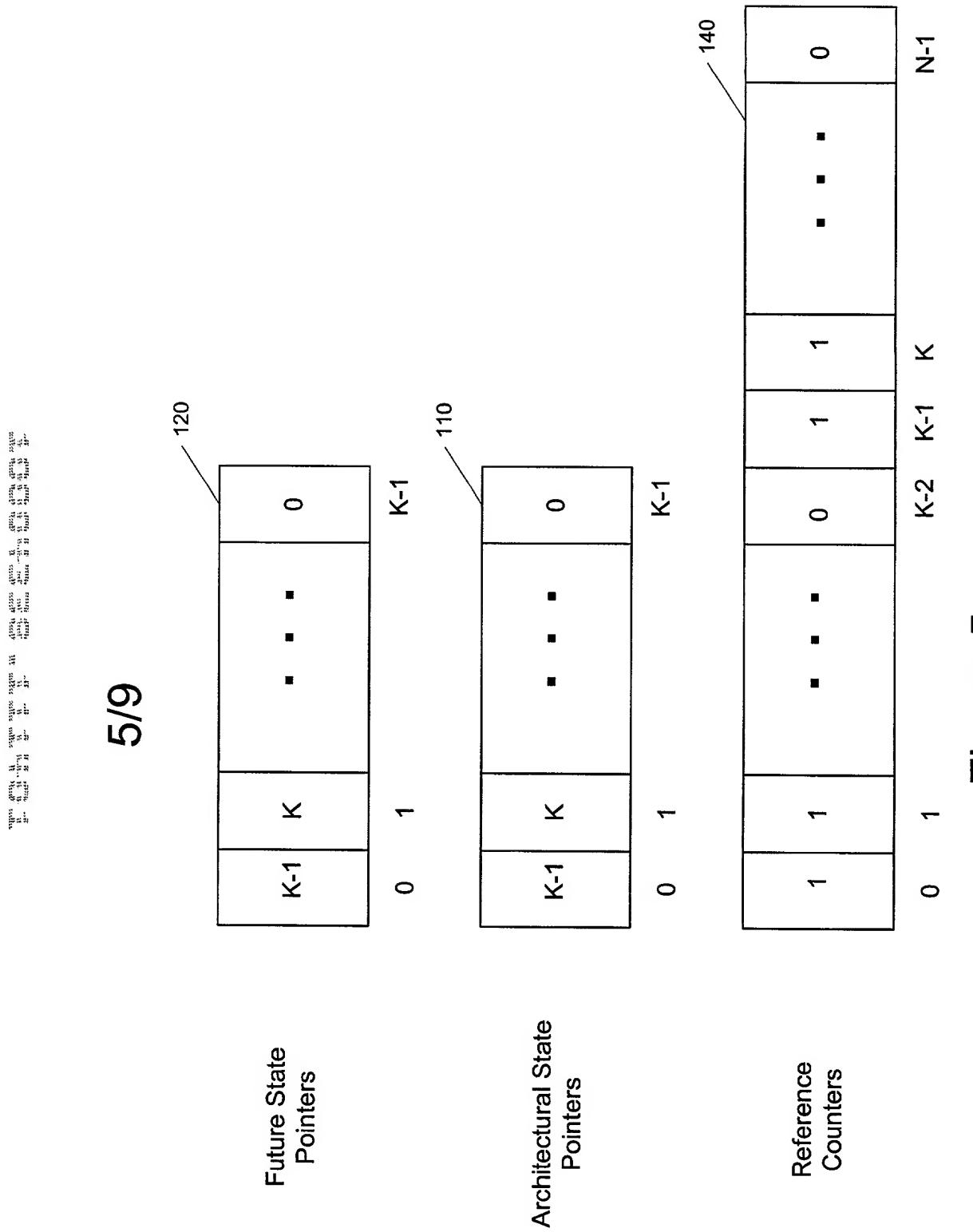


**Figure 3**

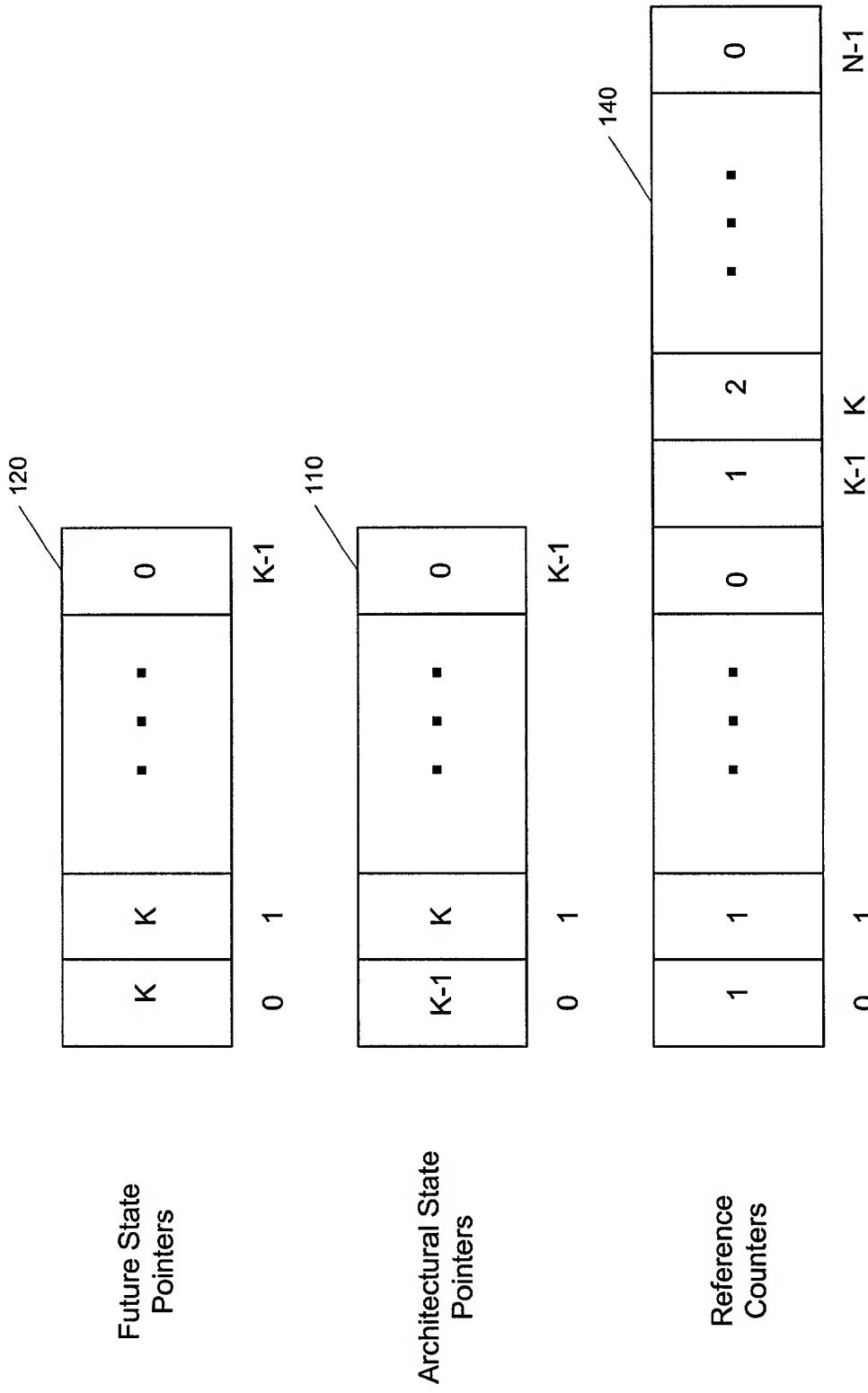
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Future State  
Pointers

O  
■ ■ ■  
K  
K

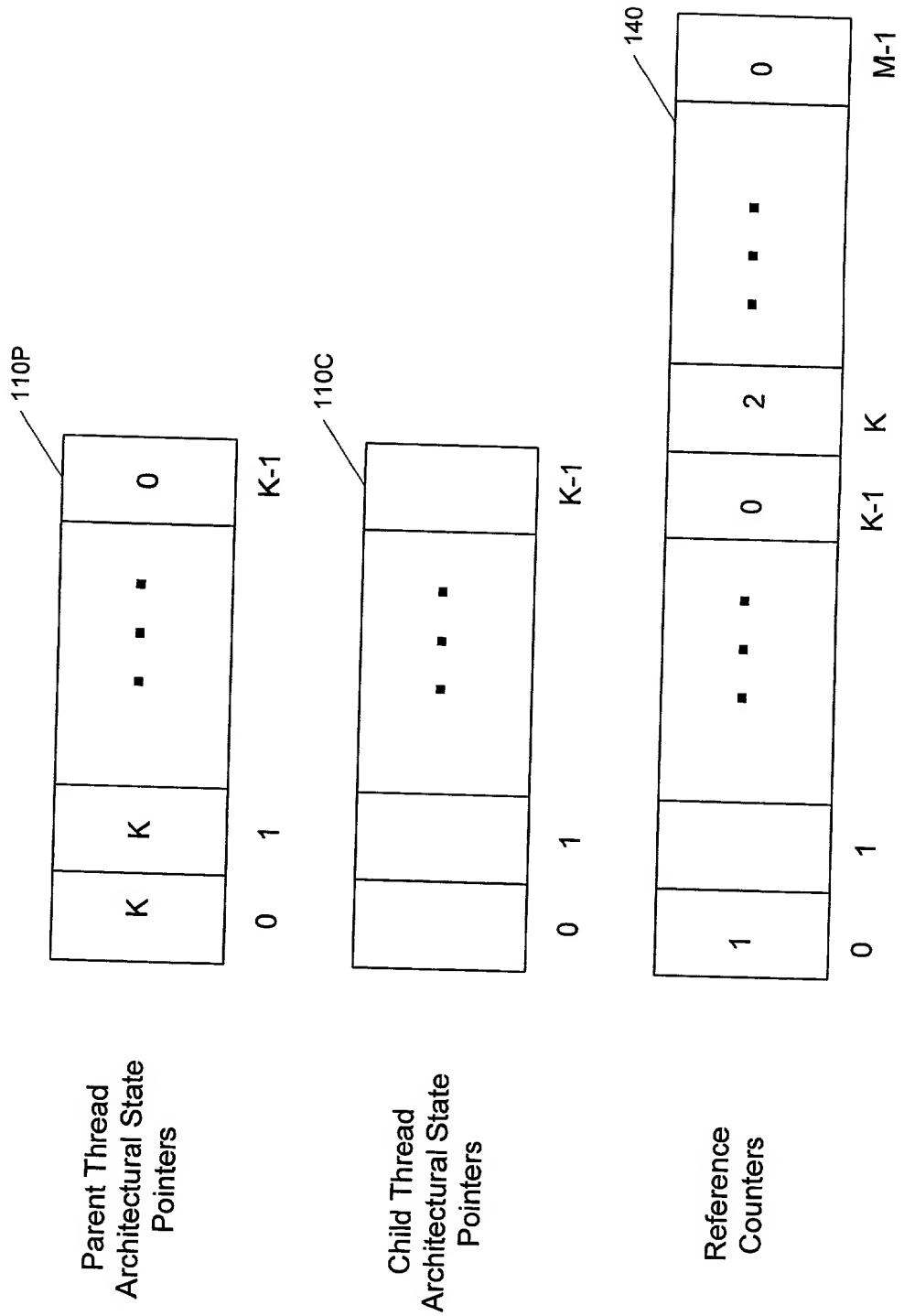
K-1

		0
	■	■
	■	K
	K	K

Architectural State  
Pointers

Figure 7

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**Figure 8**

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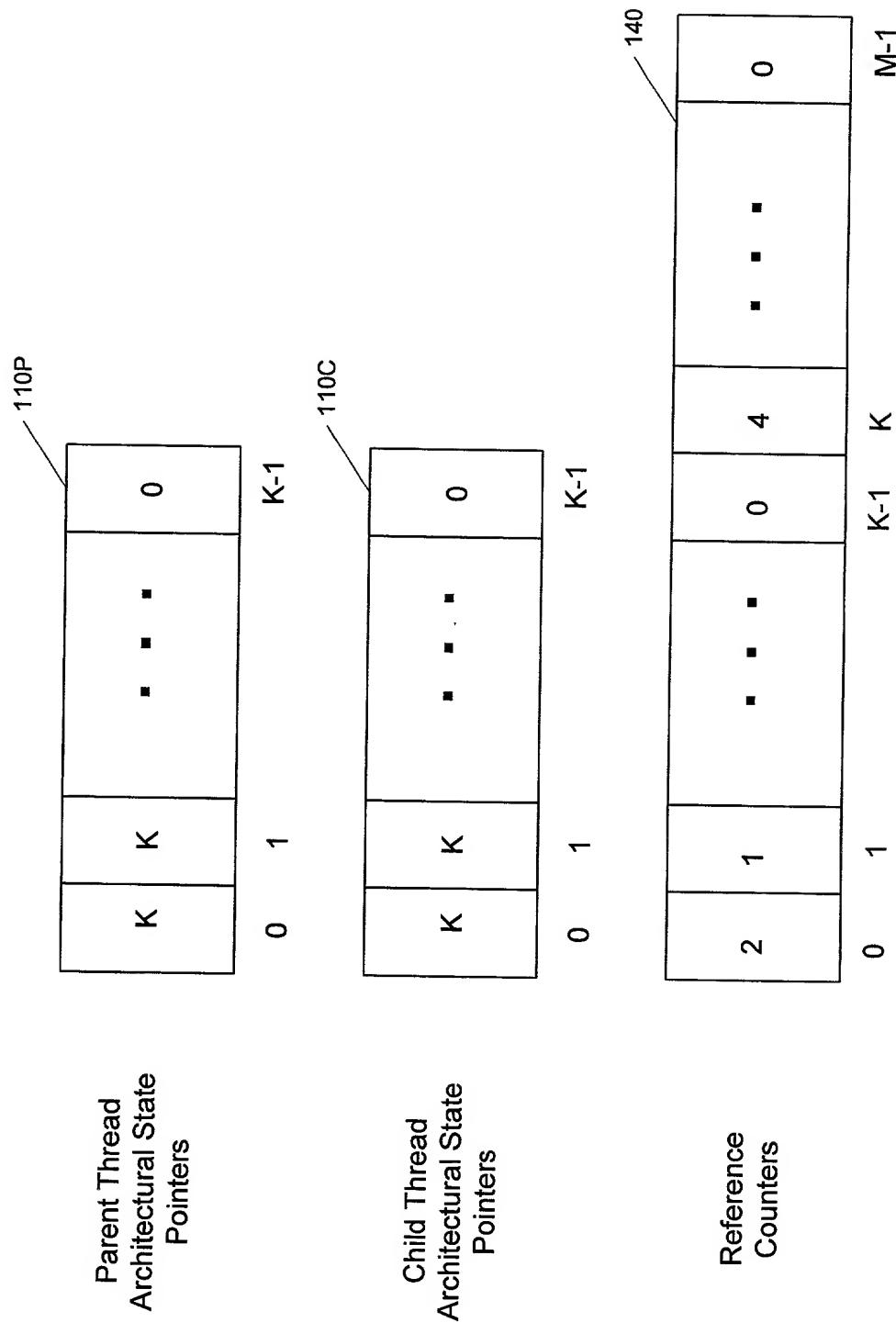


Figure 9